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DEC 02 2005

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27896 7590 10/21/2005

EDELL, SHAPIRO & FINNAN, LLC
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SUITE 400
ROCKVILLE, MD 20850

12/05/2005 MBEYENE2 00000119 10787119

01 FC:1501 1400.00 OP
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(Depositor's name)

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/787,119	02/27/2004	Joerg Vollrath	0928.0034C	5078

TITLE OF INVENTION: INTEGRATED SEMICONDUCTOR CIRCUIT HAVING A CELL ARRAY HAVING A MULTIPLICITY OF MEMORY CELLS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	01/23/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
DOAN, THERESA T	2814		257-296000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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